Fig. 1

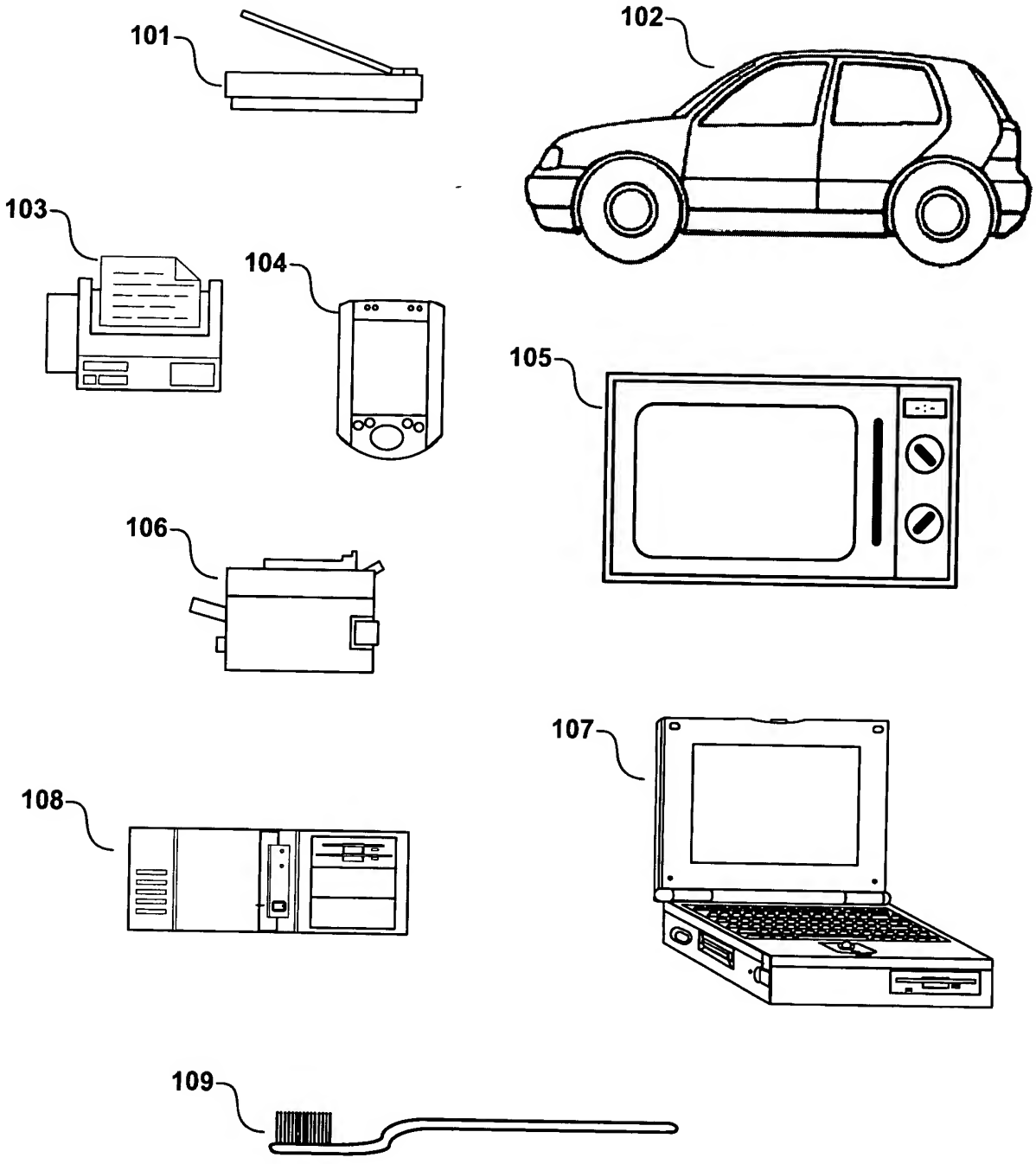


Fig. 2

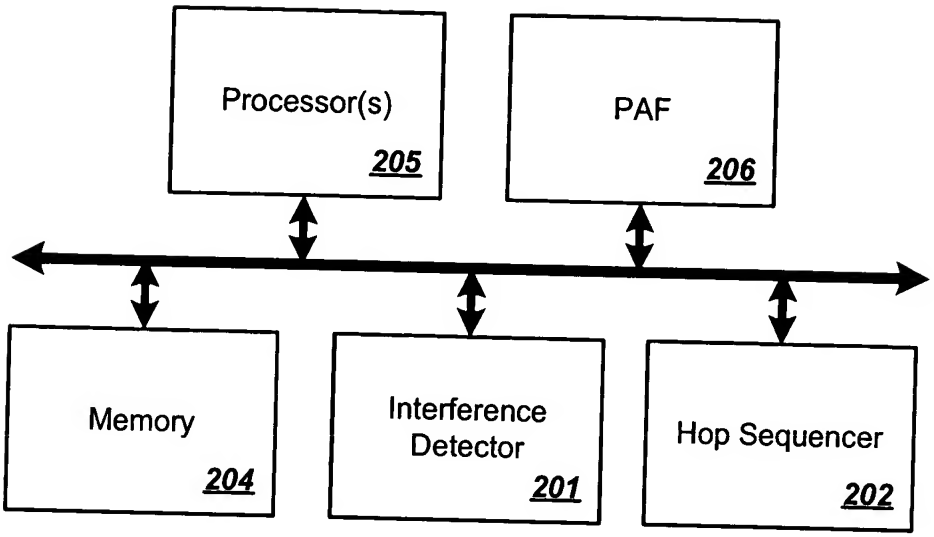

Processor(s)
205
PAF
206
Memory
204
Interference
Detector
201
Hop Sequencer
202

Fig. 3

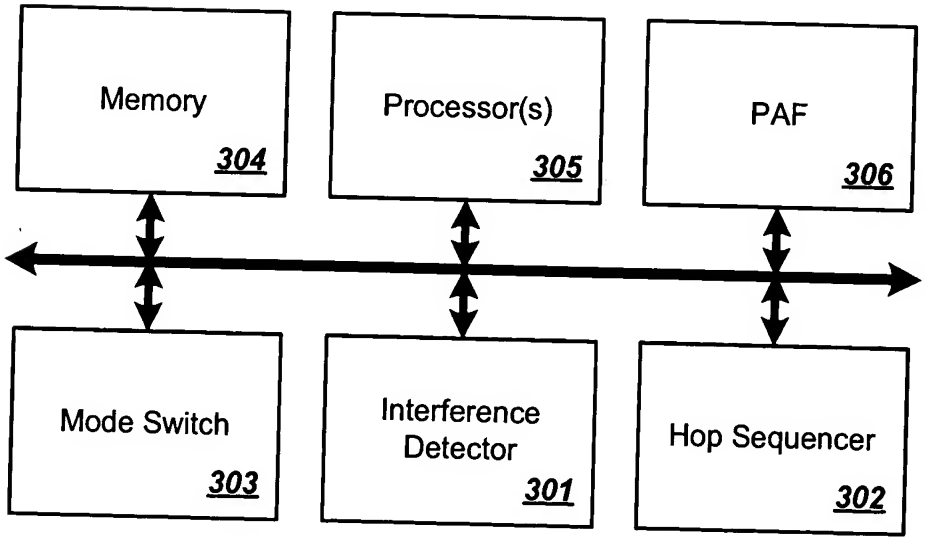
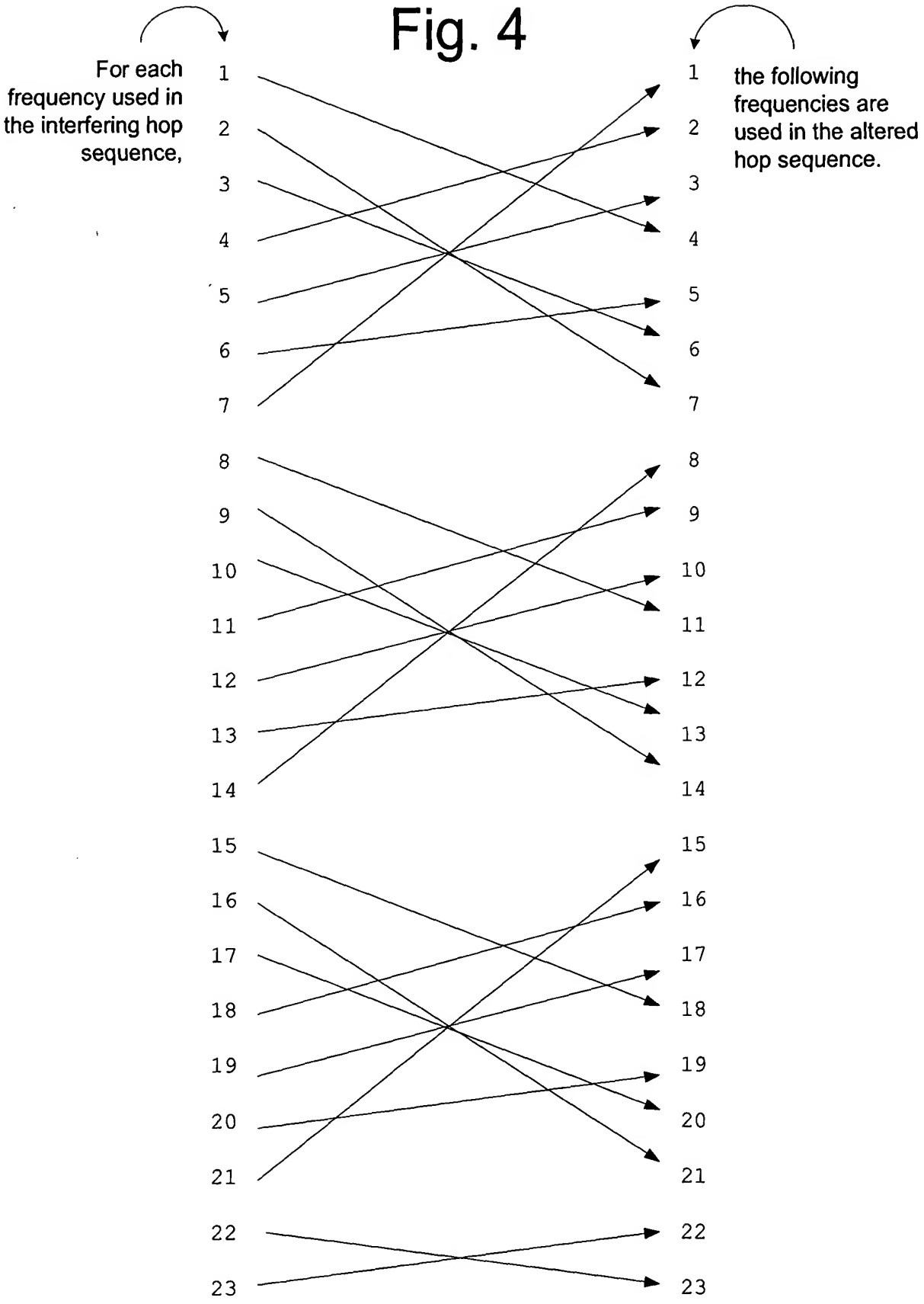

Memory
304
Processor(s)
305
PAF
306
Mode Switch
303
Interference
Detector
301
Hop Sequencer
302

Fig. 4


For each frequency used in the interfering hop sequence,
1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23
1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23
the following frequencies are used in the altered hop sequence.